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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application Of:	Kovacac et al.	Our File No.	115-01 US DIV(2)
Application No.:	09/988,938	Examiner:	Pham, Thanh V.
Filing Date:	February 01, 2002	Art Unit:	2823
Conf. No.:	9218	Class-Subclass:	257-019000
Title:	SI-GE Base Heterojunction Bipolar Device		

May 11, 2004

Mail Stop Issue Fee

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Attention: Official Draftsperson

Dear Sir:

This application is allowed and the deadline for paying the issue fee and submitting the corrected formal drawings is **May 25, 2004**.

Please find attached four (4) sheets of corrected formal drawings containing Figures 5, 6, 7, 8, 9, 10, 11, 12 and 13 to replace Figures 5-13 currently on file.

Respectfully submitted,

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